

VLVNT Workshop Amsterdam

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**Efficient high voltage voltage
supplies and dividers**

optimising photomultiplier performance at low power

- ♦ battery operated
- ♦ solar powered (Auger, satellite)
- ♦ under-water and under-ice experiments



two considerations

- ♦ consuming the power (voltage divider)
- ♦ providing the power (power supply)

voltage divider considerations

Requirement

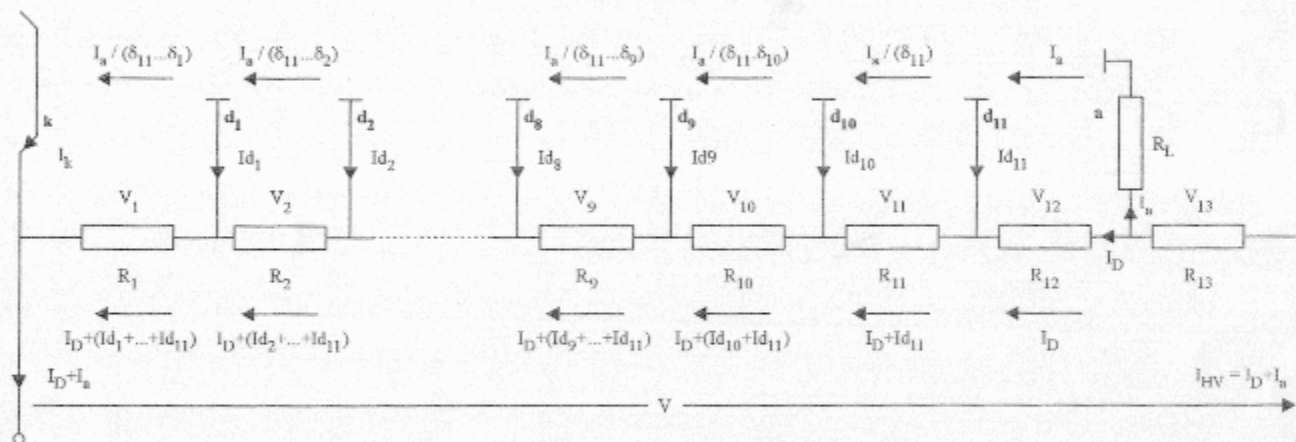
- ♦ establish & maintain a set of fixed dynode potentials

Two generic types available

- ♦ resistor type
- ♦ active divider (FET)

The all-resistor divider always fails the requirement – but why and how badly?

all-resistor divider



Simplistic explanation

$$g = a V^n$$

$$\frac{dg}{g} = \frac{n}{V} dV \quad \dots(1)$$

but $dV \approx I_a R$ where I_a is the mean anode current, $n \approx 10$ and $V \sim 1000$ volts

low power consumption dividers typically use $1 \text{ M}\Omega$ resistors and draw $100 \text{ }\mu\text{A}$ divider current, so

$$\begin{aligned} \frac{dg}{g} &= \frac{10 (10^{-6} \times 10^6)}{1000} \\ &= +1\% \text{ per } \mu\text{A of } I_a \end{aligned}$$

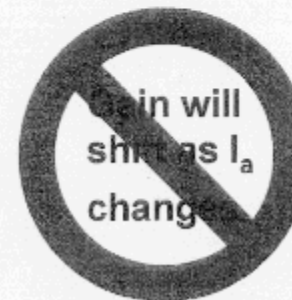
two possibilities

- ♦ light source is absolutely constant
 - mean anode current I_a does not change



problem goes away

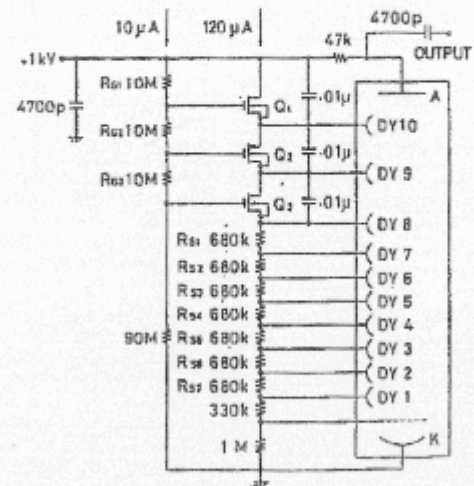
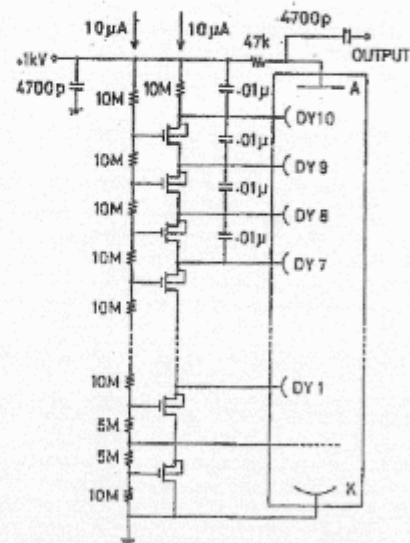
- ♦ light source varies with time constant greater than that of the voltage divider



solutions

- ♦ increase I_{D0} by decreasing all R values
- or
- ♦ reduce $R(d_n-a)$ only
- ♦ use either a fully or partially active divider based on emitter follower action
- ♦ capacitors for pulsed signals

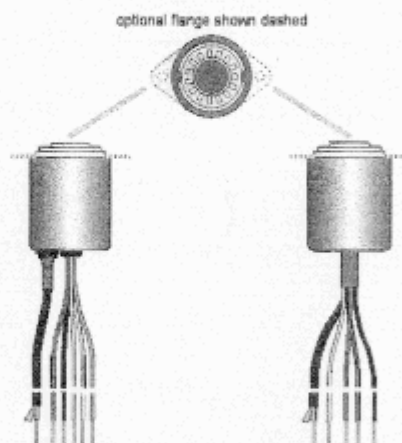
active divider networks



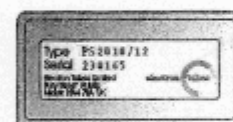
power supplies

- ♦ CW type with n individual socket outputs
- ♦ Active divider with n individual outputs
- ♦ Low power dc-dc converter with single output
- ♦ Industrial dc-dc converter with single output

power supply outlines



PS1800/PS1806



PS2010



PS2001

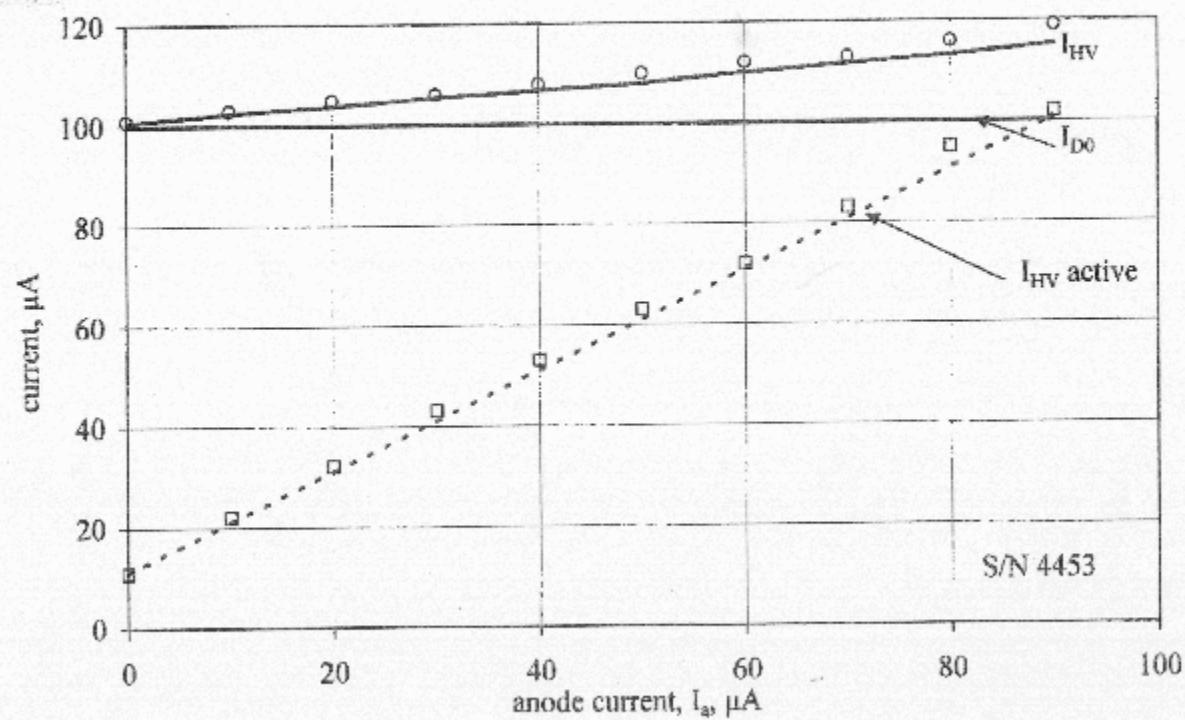


electron tubes

The diagram shows a 9-bit DAC circuit. It features a ladder network of resistors and diodes. The input is connected to the cathode (k) terminal. The output is taken from the anode (a) terminal. The circuit is labeled with diodes d_1 through d_9 and resistors $1/10 V$. The output is labeled "anode output".

[illegible]

The diagram shows a 12-bit active divider circuit. A central rectangular block is labeled "ACTIVE DIVIDER". Above this block, there are 12 input lines labeled $d_1, d_2, d_3, d_4, d_5, d_6, d_7, d_8, d_9, d_{10}, d_{11}$ and a common input line labeled a . The inputs d_1 through d_{11} are connected to a supply voltage of $2/12\text{ V}$, while the common input a is connected to $1/12\text{ V}$. The output of the divider is labeled "anode output".



power supplies

Type	Input power (mW)	Efficiency	Output current
PS1800	120	-	$I_a = 200 \mu\text{A}$
PS1806	400	-	$I_a = 200 \mu\text{A}$
PS2010	340	60%	$I_{D0} = 100 \mu\text{A}$
PS2001	4800	40%	$I_{D0} = 1000 \mu\text{A}$